

S49: (3010) (carryout or carry-out or carry) adj generat\$3
S50: (317217) instruction
S51: (1) S49 adj S50
S52: (14323) (carryout or carry-out or carry) near3 generat\$3
S54: (0) S53.ti.
S55: (0) S53.ab.
S53: (49) S49 near3 S50
S56: (282) (carryout or carry-out or carry) adj instruction
S57: (3) S56.ti.
S58: (27) S56.ab. not S57
S59: (0) packed near5 S56
S60: (0) packed with S56
S61: (0) packed same S56
S62: (81) packed and S56
S63: (3739472) adding or add or addition\$1
S64: (224333) packed
S65: (3428) S63 near10 S64
S66: (139389) overflow\$1 or over-flow\$1 or over adj flow\$1
S67: (1091349) (carryout or carry-out or carry)
S68: (3554) S66 with S67
S69: (3) S65 same S68
S70: (84) S65 and S68
S71: (6) ("5,132,921" or "6,263,424" or "5,132,921" or "4,893,267").PN

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US-PGPUB: USPAT, USOCR, EPO, JPO, DERWE

Default operator:

OR

☒ Highlight all hit terms initially

5,132,921
6,263,424
5,132,921
4,893,267

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cl	Inventor	S	C	F
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6263424 B1	20010717	11	Execution of data dependent arithmetic instructions in multi-pipeline processors	712/221	708/706		Tran, Dzung X. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 5132921 A	19920721	16	High speed digital computing system	708/701	708/708		Kelley, Edwin A. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 4893267 A	19900109	5	Method and apparatus for a data processor to support multi-mode, multi-precision integer	708/518			Alsup, Mitchell et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6263424 B	20010717	11	Multi-pipeline processor uses single port arithmetic logic unit in one pipeline to obtain				MUNSON, K K et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	US 4893267 A	19900109	5	Processor support for multi-mode multi-precision integer arithmetic - enables				ALSUP, M et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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